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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/826,034	04/16/2004	Naoyuki Koizumi	CU-3692 RJS	9058
26530 759	90 07/26/2005	•	EXAM	INER
LADAS & PARRY LLP			HA, NATHAN W	
224 SOUTH MICHIGAN AVENUE SUITE 1600			ART UNIT	PAPER NUMBER
CHICAGO, IL 60604			2814	
			DATE MAILED: 07/26/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/826,034	KOIZUMI ET AL.				
Office Action Summary	Examiner	Art Unit				
	Nathan W. Ha	2814				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. C (35 U.S.C. § 133).				
Status						
1)	s action is non-final. ince except for formal matters, pro					
Disposition of Claims	•					
4) ☐ Claim(s) 1-15 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-15 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/o	wn from consideration.					
Application Papers						
9) The specification is objected to by the Examina 10) The drawing(s) filed on is/are: a) accomplicated any not request that any objection to the Replacement drawing sheet(s) including the correct to by the E	cepted or b) objected to by the lead of a drawing(s) be held in abeyance. Section is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119		•				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 4/04.	4) Interview Summary Paper No(s)/Mail D: 5) Notice of Informal F 6) Other:					

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DETAILED ACTION

Specification

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- 3. Claims 1-15 are rejected under 35 U.S.C. 102(a) as being anticipated by Taniquchi et al. (US 2003/0116857, hereinafter, Taniquchi.)

In regard to claims 1 and 10, in figs. 2A-3D, Taniguchi discloses a method for manufacturing a semiconductor pacjage, comprising:

attaching a bottom surface of a semiconductor wafer 10 to a first supporting member 32;

forming a through hole 12 in the wafer;

separating the wafer from the supporting member, see fig. 2B;

forming an insulating layer14a on at least the bottom surface of the wafer and inner wall of the through hole, see fig. 2C;

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forming a conductive layer 28 underneath the wafer, the conducting layer spanning at least the bottom of the through hole; and

forming a conductive member 18a-c in the through hole and electrically contact the conducting layer, see fig. 3D.

In regard to claims 2 and 11, Taniguchi further discloses that the substrate is etched using RIE process. This process is a step of thin-filming the substrate, wafer (see section [0063].)

In regard to claims 3 and 12, the conductive member is formed by plating (see section [0079].)

In regard to claims 4 and 5, Taniguchi further discloses a step of removing at least a portion of the conductive layer and left a portion on the substrate, see fig. 3D.

In regard to claim 6, the conducting layer is a tape or film member (see section [0074].)

In regard to claims 7 and 13, the through hole is formed by etching (see section [0063].)

In regard to claims 8 and 14, layer 14a is inorganic layer and layer 20 formed on top is an organic layer (see fig. 3D and section [0057].)

In regard to claims 9 and 15, layer 34 functions as a barrier layer, see fig. 3D.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nathan W. Ha whose telephone number is (571) 272-1707. The examiner can normally be reached on M-TH 8:00-7:00(EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nathan Ha

July 23, 2005

John W. tta